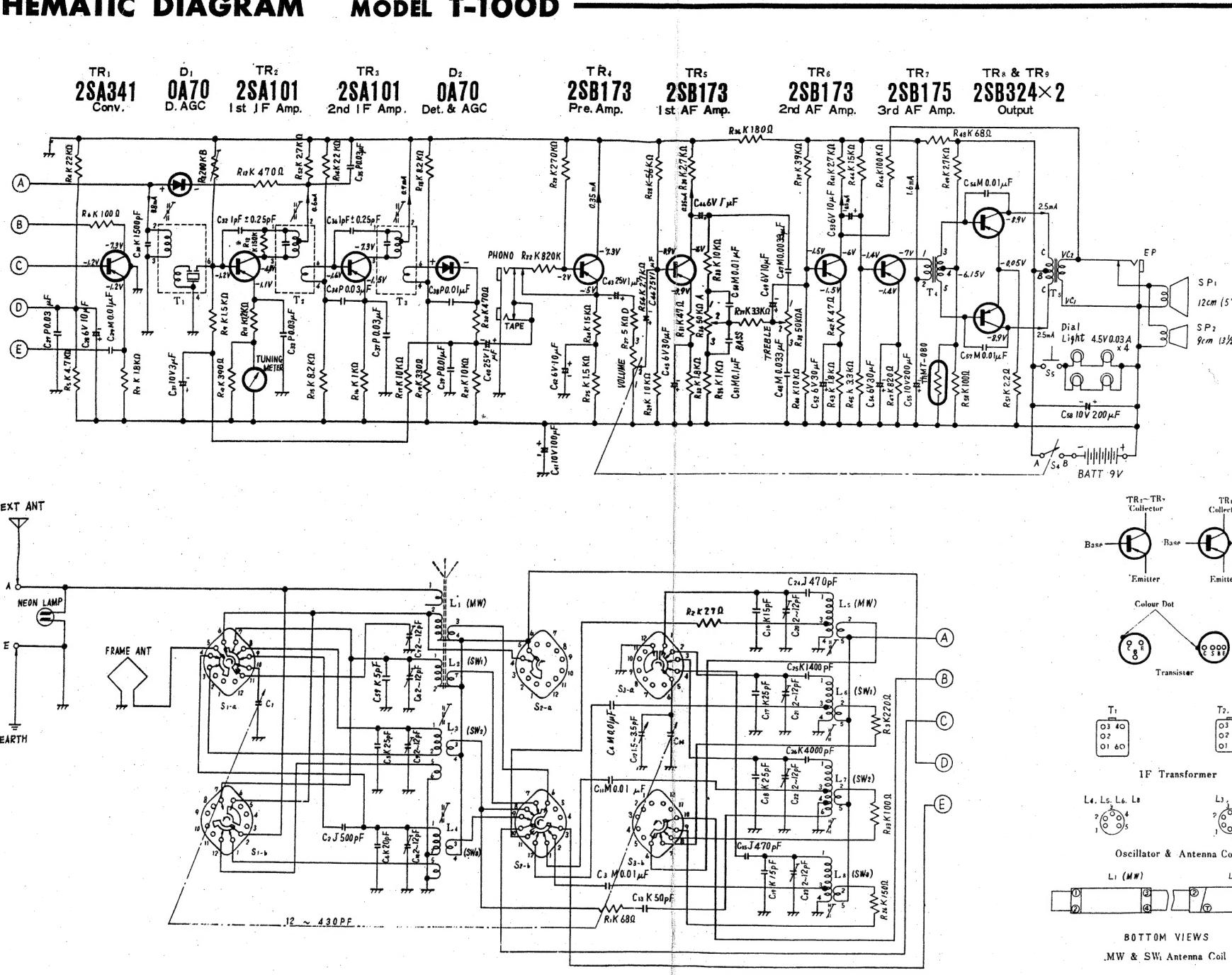




## SCHEMATIC DIAGRAM MODEL T-100D



### Notes:

1. S<sub>1</sub>~S<sub>3</sub>: Band selector switch in "MW" position.
2. S<sub>4</sub> : Power source switch in "OFF" position.
3. S<sub>5</sub> : Dial lamp switch in "OFF" position.
4. Tone controls: R<sub>34</sub> BASS  
R<sub>38</sub> TREBLE
5. Measured voltages for TR<sub>1</sub>~TR<sub>9</sub> are from transistor terminal to bias line. [A]
6. DC Voltage measurements are taken with circuit tester (10kΩ/V).
7. Capital letters (M, K, J, P) in the circuit diagram show allowable tolerance of resistors and capacitors as follows.  
M=±20% K=±10% J=±5% P=+100% - 0%
8. Battery Current:  
No signal ..... 23mA  
Maximum output..... 300mA
9. PF=pico farad=mmf  
μF=micro farad=MF

NATIONAL PANASONIC RADIO

# CIRCUIT BOARD

MODEL T-100D

Conductor View

